

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. HORIKOSHI et al
 Serial No.: 09/988,585
 Filed: November 20, 2001
 For: THIN-FILM TRANSISTOR AND METHOD OF
 MANUFACTURING THE SAME
 Group: 2811
 Examiner: V. TRAN

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & §1.98

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

May 21, 2004

Sir:

In the matter of the above-identified application, applicants, through their undersigned representative, are submitting herewith copies and a listing therefor, in form PTO/SB/08a, of two (2) U.S. Patents and an English language publication that are of record in presently allowed U.S. Patent Application Serial No. 10/166,311, filed June 11, 2002, which is a divisional application of the present application (the claims thereof are drawn to a method), in keeping with the duty of disclosure/candor requirements and in order to complete the record. There are also listed the two (2) publications which were earlier discussed and copies of which were submitted in connection with the amendment filed on December 19, 2002. However, a listing thereof was not included therewith, at that time.

This information disclosure statement is being submitted prior to the mailing of either a final action on the merits or an official Notice of Allowance and, accordingly, is in compliance with 37 CFR §1.97(c). An appropriate fee directed thereto is also being

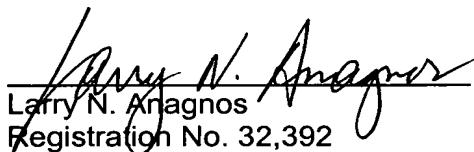
submitted herewith.

As to the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance to the extent that the listed publication directed to the handbook on Liquid Crystal Display Panel Manufacturing Technique is not in the English language, this is met by: the submission of an English translation of Table III and of a part of the description thereof as well as the pertinent discussion earlier included in the Remarks on page 14 of the Amendment filed on December 19, 2002 as well as in the first full paragraph on page 8 of the presently filed Declaration (under 37 CFR §1.132).

It is submitted this IDS is in compliance with the rules of practice as well as with USPTO guidelines. Therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgement of such entry as well as of formal consideration thereof by the Examiner is respectfully requested.

Please charge any shortage in the fees due in connection with the filing of this paper to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 566.40894X00), and please credit any excess fees to such deposit account.

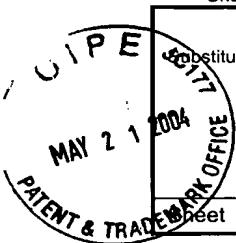
Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



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Attachments

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/988,585
				Filing Date	November 20, 2001
				First Named Inventor	HORIKOSHI, Kazuhiko et al
				Art Unit	2811
				Examiner Name	T. TRAN
Sheet	1	of	2	Attorney Docket Number	566.40894X00

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY		
		US- 6,541,278	04/2003	Morita et al	
		US- 6,127,279	10/2000	T. Konuma	

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
		"A Handbook on Liquid Crystal Display Panel Manufacturing Technique," p. 193, 1992, Science Forum. [partial English translation included]	
		ANMA, M. "A New Measurement of Thermal Dimensional Stability of Glass and Its Application to LCD Substrates," Glass Technology, Vol. 40, No. 4, August 1999.	
		TSENG et al., "Thin CVD Stacked Gate Dielectric for ULSI Technology," 1993, IEDM 93, pp. 321-324.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.